

Docket No.: GR 97 P 1903

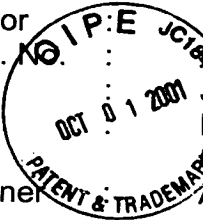
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By: Markus Nollf

Date: Sept 26, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor Hansjörg Reichert et al.
Applic. No. 09/483,737
Filed January 14, 2000
Title Method and Apparatus for Producing a Chip-Substrate Connection
Examiner Ahmed N Sefer - Art Unit: 2826



RESPONSE

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated September 18, 2001, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 11-16, for prosecution at this time.

Applicants reserve the right to file a divisional application including the method claims of group II at a later date and/or to request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 11-16 is solicited.

Respectfully submitted,

Markus Nollf
For Applicants

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Date: September 26, 2001

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